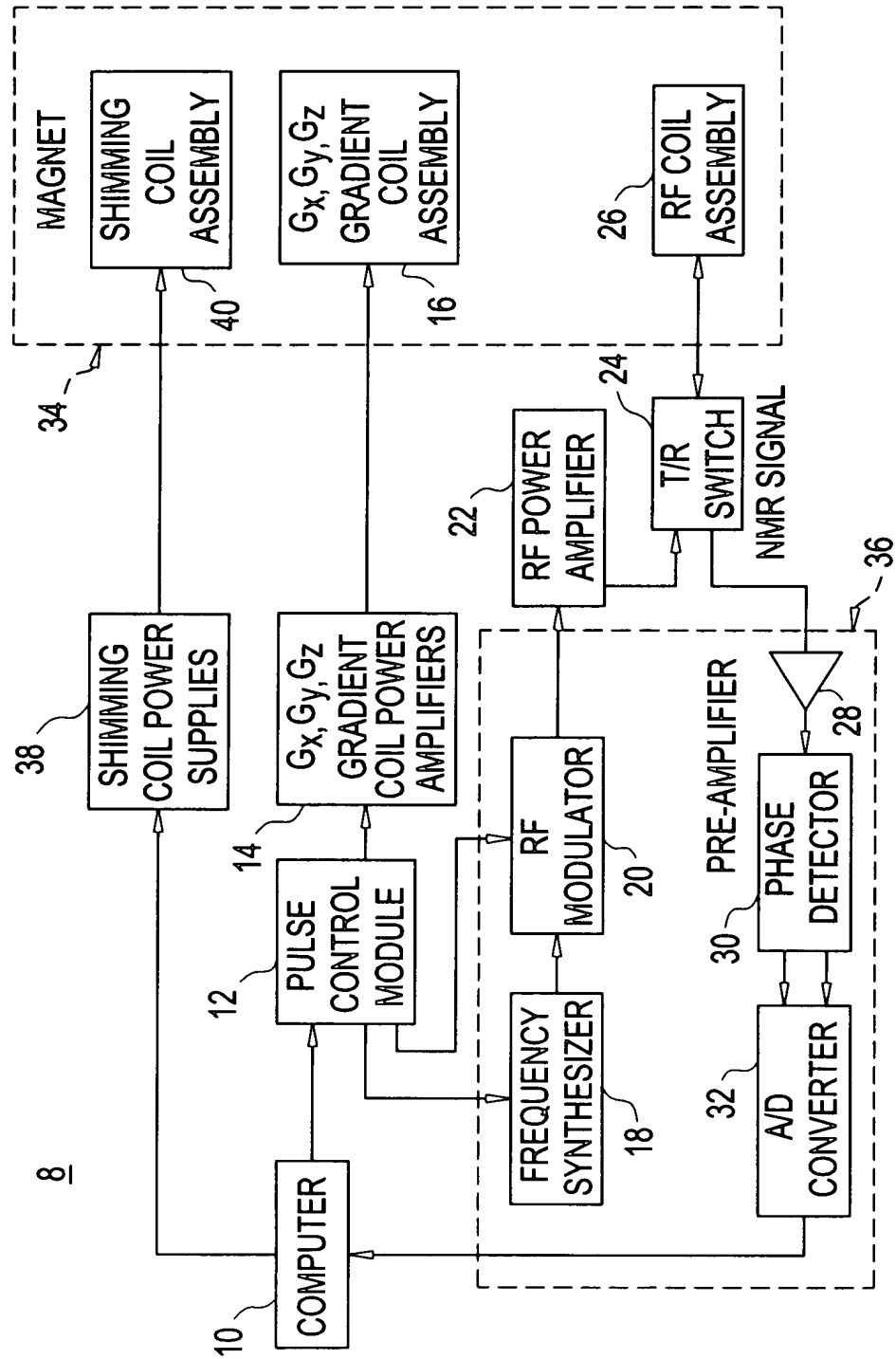


FIG. 1



A plan view of a semiconductor device. It features a central rectangular gap. The left and right sides of the device are defined by vertical lines labeled 110. The top and bottom edges of the device are defined by horizontal lines labeled 112. There are electrical contacts or vias at the corners and midpoints of the outer edges, also labeled 112. A horizontal arrow labeled X points to the right, indicating a direction of interest.

FIG. 4

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